

Services and Price Guide

Digital chip design, verification and FPGA prototyping, priced as fixed-price milestones.

Naple Semiconductor designs and verifies digital logic for engineering teams that need extra capacity or specialist help. Every design is checked against an independent reference model, and our own Raven-01 sensor chip design was verified this way in simulation. The prices below are guides. Your final price is fixed in a Statement of Work once we have agreed the specification.

Standard engineering day rate

US\$400

about €360 · US\$50 per hour

Founding client rate (first 3 clients)

US\$300

about €270 · 25% off every package

Scoping and specification

US\$800

2 days · credited in full if you go ahead

Euro figures are approximate, at €1 = US\$1.12 (21 October 2026). Quotes are issued in US\$, €, £ or ZAR at the rate on the quote date. A first 30-minute discovery call is free.

1. Design and verification packages

Package	Typical effort	Standard price	Founding client
A1 Interface and peripheral block UART, SPI, I ² C, PWM, timers, GPIO, CRC and checksum engines. RTL, a self-checking testbench and a verification report.	4 to 8 days	US\$1,600 to 3,200	US\$1,200 to 2,400
A2 Sensor chip logic Digital logic for sensor chips: reading 1 to 4 sensors, filtering (median, moving average), threshold and event detection, and packet output. The same class of design as our Raven-01.	12 to 25 days	US\$4,800 to 10,000	US\$3,600 to 7,500
A3 Verification of your RTL A test plan, testbench, independent reference model, regression suite and coverage report for a block you designed.	6 to 15 days	US\$2,400 to 6,000	US\$1,800 to 4,500
A4 RTL audit and lint report A review of your code for bugs, unsafe constructs, clock-domain issues and style, with a prioritised fix list.	3 to 6 days	US\$1,200 to 2,400	US\$900 to 1,800
A5 FPGA proof sprint A verified design taken onto your FPGA board in 2 to 3 weeks: synthesis, timing closure, bring-up and a recorded demonstration. You lend us the board, or we agree one.	10 to 15 days	US\$4,000 to 6,000	US\$3,000 to 4,500

2. Larger projects (priced after scoping)

Project	Typical effort	Standard price	Founding client
B1 Microcontroller-class SoC on FPGA A small system-on-chip built around a proven open-source RISC-V core, with a bus, memory, 3 to 5 peripherals, a boot ROM and a first test program.	25 to 50 days	US\$10,000 to 20,000	US\$7,500 to 15,000
B2 DSP accelerator Fixed-function signal processing in hardware (FIR or IIR filters, CORDIC, small FFTs), with a fixed-point reference model.	15 to 35 days	US\$6,000 to 14,000	US\$4,500 to 10,500
B3 Chip layout for a shared wafer run Taking a verified design through an open-source flow (OpenLane) to a layout file that passes the foundry's checks, ready for a multi-project wafer shuttle on an open process such as SKY130, GF180 or IHP. Fabrication is charged separately (section 3).	15 to 30 days	US\$6,000 to 12,000	US\$4,500 to 9,000

3. Silicon and third-party costs (at cost, plus 10% handling)

Fabrication on shared open-process shuttles (for example Tiny Tapeout, ChipFoundry or wafer.space), packaging, boards, and any commercial tool licences are passed through at the provider's current price plus a 10% handling fee. We confirm each one in writing before you commit. Shuttle operators set their own prices and dates, and a shuttle can be delayed if it is not full.

4. Not currently offered

We don't take on the following, because we would not deliver them to the standard you need today. We will tell you so before you spend anything:

- Graphics processors (GPUs) and high-performance CPUs (out-of-order, multi-core, application-class);
- analogue, mixed-signal, RF and power-management chips (ADCs, PLLs, radios, regulators);
- sign-off on advanced commercial process nodes;
- designs that need safety certification (for example ISO 26262 or medical-device standards); and
- cryptocurrency-mining or other crypto-asset hardware, which we never accept.

5. How pricing works

Fixed price per milestone	Each project is split into milestones with defined deliverables and objective acceptance tests. You pay for results, not hours.
Deposit	40% of the first project's price is paid on signature. Work starts when the deposit arrives.
Payment	Invoices are due within 14 days, by international bank transfer. The sender pays bank charges (SWIFT option "OUR").
Changes	Changes to an agreed scope are priced at the day rate and recorded in a signed change order.

Ownership	You own the design created for you once it is paid in full.
Quotes	Valid for 30 days. Naple is not registered for VAT, so no South African VAT is added.
Founding Client Programme	For our first three clients: 25% off any package. In return, we ask for an honest testimonial and, only with your written permission, a short case study that reveals nothing confidential.

Start a project: email hello@napletech.online with a short description of what you need (a draft specification is ideal) to book a free 30-minute discovery call. We reply within two working days.
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